

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Takeshi TAKAGI et al.

Serial No.: 09/659,484

Filed: September 11, 2000

For: BIPOLAR TRANSISTOR AND
SEMICONDUCTOR DEVICE



Examiner: Wai Sing LOUI

Group Art Unit: 2814

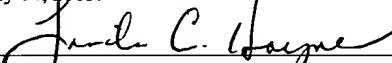
Confirmation No.: 9022

Date: July 14, 2003

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 14, 2003.

Signature: 

Name: Linda C. Haynes

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
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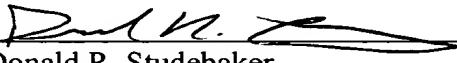
Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of the document cited on the PTO-1449 Form is enclosed and described in the specification of the above-captioned patent application.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

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